

REMARKS

I. Introduction

With the addition of new claims 18 to 21, claims 1 to 21 are currently pending in the present application. In view of the foregoing amendments and the following remarks, it is respectfully submitted that all of the presently pending claims are allowable, and reconsideration is respectfully requested.

Applicant notes with appreciation the acknowledgement of the claim for foreign priority and the indication that all copies of the certified copies of the priority documents have been received.

Applicant thanks the Examiner for considering the previously filed Information Disclosure Statement, PTO-1449 papers, and cited references.

II. Objection to Claims 4 to 6 and 8 to 15

Claims 4 to 6 and 8 to 15 were objected to under 37 C.F.R. § 1.75(c). Claims 4 to 6 and 8 to 15 have been amended herein without prejudice to obviate the present objection. Withdrawal of the objection is therefore respectfully requested.

III. Rejection of Claims 12 and 17 Under 35 U.S.C. § 112

Claims 12 and 17 were rejected under 35 U.S.C. § 112, ¶ 2 as assertedly indefinite. Claims 12 and 17 have been amended herein without prejudice to obviate the present rejections. Withdrawal of the indefiniteness rejection of claims 12 and 17 is therefore respectfully requested.

IV. Rejection of Claims 1 to 3, 7, 16, and 17 Under 35 U.S.C. § 102(b)

Claims 1 to 3, 7, 16, and 17 were rejected under 35 U.S.C. § 102(b) as anticipated by U.S. Patent Application Publication No. 2002/0103839 ("Ozawa"). It is respectfully submitted that Ozawa does not anticipate any of claims 1 to 3, 7, 16, and 17 for at least the following reasons.

Claim 1, as herein amended without prejudice, relates to a data processing arrangement, and recites, *inter alia*, the following:

... a cell element field including ... for at least one of [a] plurality of function cell arrangements [of the cell element field] and at least one of [a] a plurality of memory cell arrangements [of the cell element field], a connection via which the at least one function cell arrangement is connectable with the at least one memory cell arrangement to form a function cell-memory cell combination that is

configured to function as a sequencer structure that provides control commands from the at least one function cell arrangement to the at least one memory cell arrangement via the connection.

Accordingly, the features of claim 1 provide an advantageous arrangement of a sequencer control command line within the cell element field, which allows for providing, with very little hardware expense, a large number of sequencers within the cell element field and, thus, highly increased functionality.

The Office Action refers to the arithmetic device 47 and the data memory 46 shown in figure 12 of Ozawa as assertedly disclosing the at least one function cell arrangement and the at least one memory cell arrangement, respectively. However, nowhere does Ozawa disclose, or even suggest, a command line between the arithmetic device 47 and the data memory 46 or that the combination of the arithmetic device 47 and the data memory 46 forms a sequencer structure. Ozawa merely provides for a connection between the arithmetic device 47 and the data memory 46 such that data “read_dat” read from a memory location of the data memory can be input to the arithmetic device 47 and results data “write_dat” output by the arithmetic device 47 can be input to the data memory 46 for storage therein. Ozawa provides for a separate selection device 44 to select configurations in accordance with signals received from a CPU 41 and an address generation device 45. The address generation device 45 generates the addresses based on signals from the CPU 41. Indeed, nowhere does Ozawa disclose any arrangement including a cell element field including function and memory cell arrangements which form sequencer structures. Accordingly, Ozawa does not disclose, or even suggest, a function cell-memory cell combination of a cell element field that is configured to function as a sequencer structure, as provided for in the context of claim 1.

Thus, Ozawa does not disclose, or even suggest, all of the features recited in claim 1, so that Ozawa does not anticipate claim 1 or its dependent claims, e.g., claims 2, 3, and 7.

Claim 16, as herein amended without prejudice, relates to a method for operating a data processing arrangement, and recites, *inter alia*, the following:

... operating at least one of [a] plurality of function cell arrangements of [a multidimensional] cell element field as a sequencer to output control commands to at least one of [a] plurality of memory cell arrangements of the cell element field.

As set forth above in support of the patentability of claim 1, Ozawa does not disclose, or even suggest, these features. Since Ozawa does not disclose, or even suggest, all of the features recited in claim 16, it is therefore respectfully submitted that Ozawa does not anticipate claim 16 or its dependent claims, e.g., claim 17.

Withdrawal of the anticipation rejection of claims 1 to 3, 7, 16, and 17 is therefore respectfully requested.

V. Claims 4 to 6 and 8 to 15

Claims 4 to 6 and 8 to 15 ultimately depend from claim 1, so that Ozawa does not anticipate any of these dependent claims for at least the same reasons set forth above in support of the patentability of claim 1.

VI. New Claims 18 to 21

Claims 18 to 21 have been added herein. It is respectfully submitted that new claims 18 to 21 do not add any new matter and are fully supported by the present application, including the specification and the drawings.

Claims 18 and 19 depend from claim 1 and are therefore allowable over the cited reference for at least the same reasons set forth above in support of the patentability of claim 1.


Claims 20 and 21 depend from claim 1 and are therefore allowable over the cited reference for at least the same reasons set forth above in support of the patentability of claim 1.

VII. Conclusion

In light of the foregoing, it is respectfully submitted that all of the presently pending claims are in condition for allowance. Prompt reconsideration and allowance of the present application are therefore earnestly solicited.

Respectfully submitted,

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